

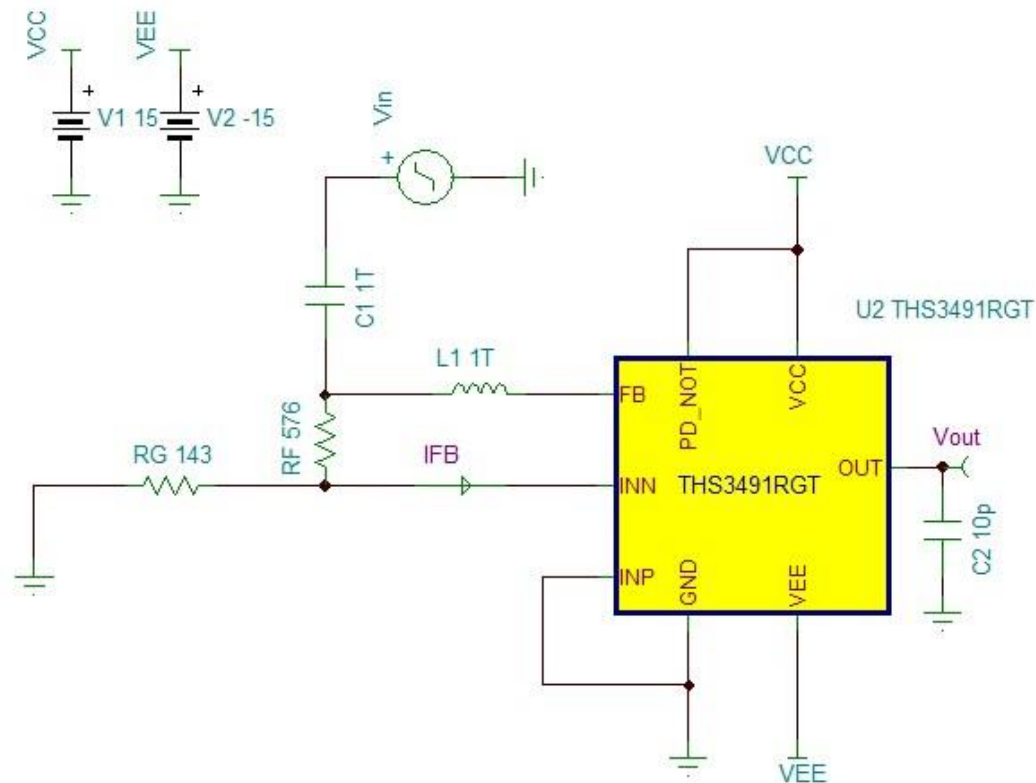
Problems

Current-Feedback Amplifiers – Part 3

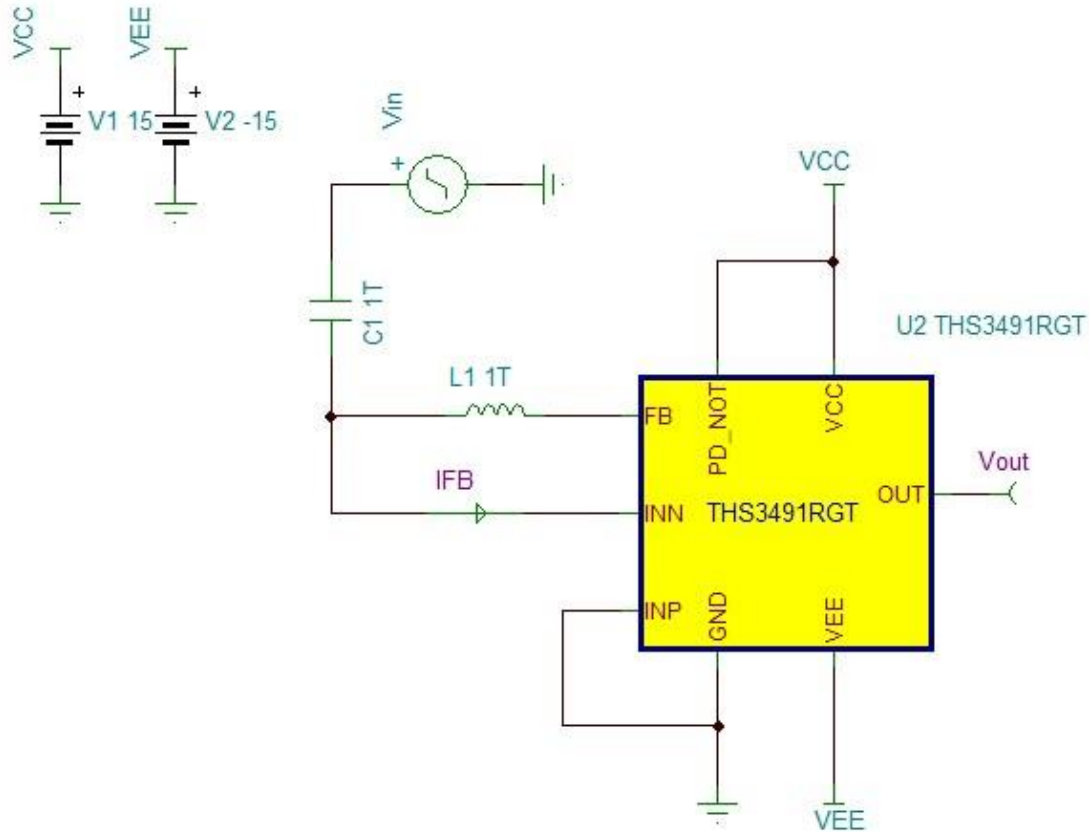
1. Simulate the Loop-Gain ($A_{ol}\beta$) Phase Margin for the circuit below with the following capacitive loads:
 - a.) 1pF
 - b.) 10pF
 - c.) 50pF



Problem1.TSC



2. Simulate the Loop-Gain ($A_{ol}\beta$) Phase Margin for the circuit below.



Problem2.TSC

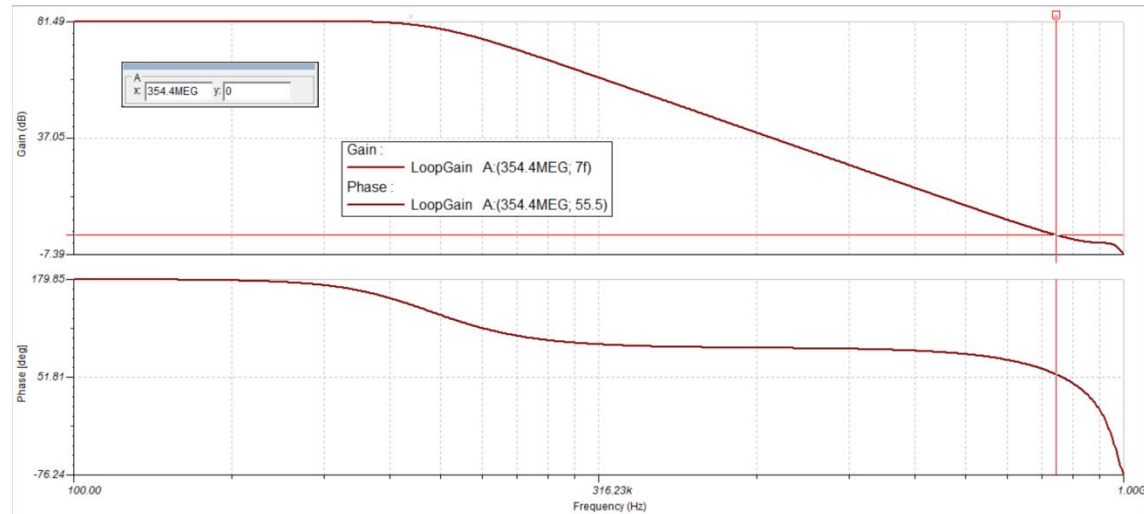
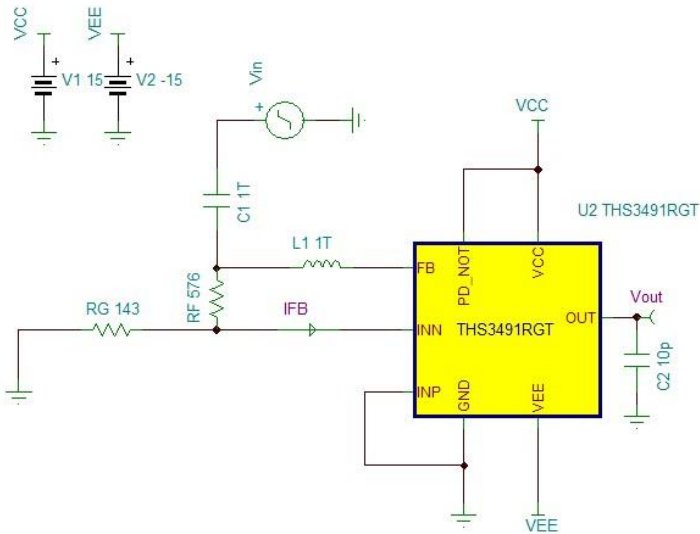
Solutions

1. Simulate the Loop-Gain ($Aol\beta$) Phase Margin for the circuit below with the following capacitive loads:

- a.) 1pF **60.03°**
- b.) 10pF **55.46°**
- c.) 20pF **44.11°**



Problem1_Solution.TSC



2. Simulate the Loop-Gain ($Aol\beta$) Phase Margin for the circuit below.

Phase Margin = -151.5° . Amplifier needs feedback resistor for proper unity gain configuration.



Problem2_Solution.TSC

