

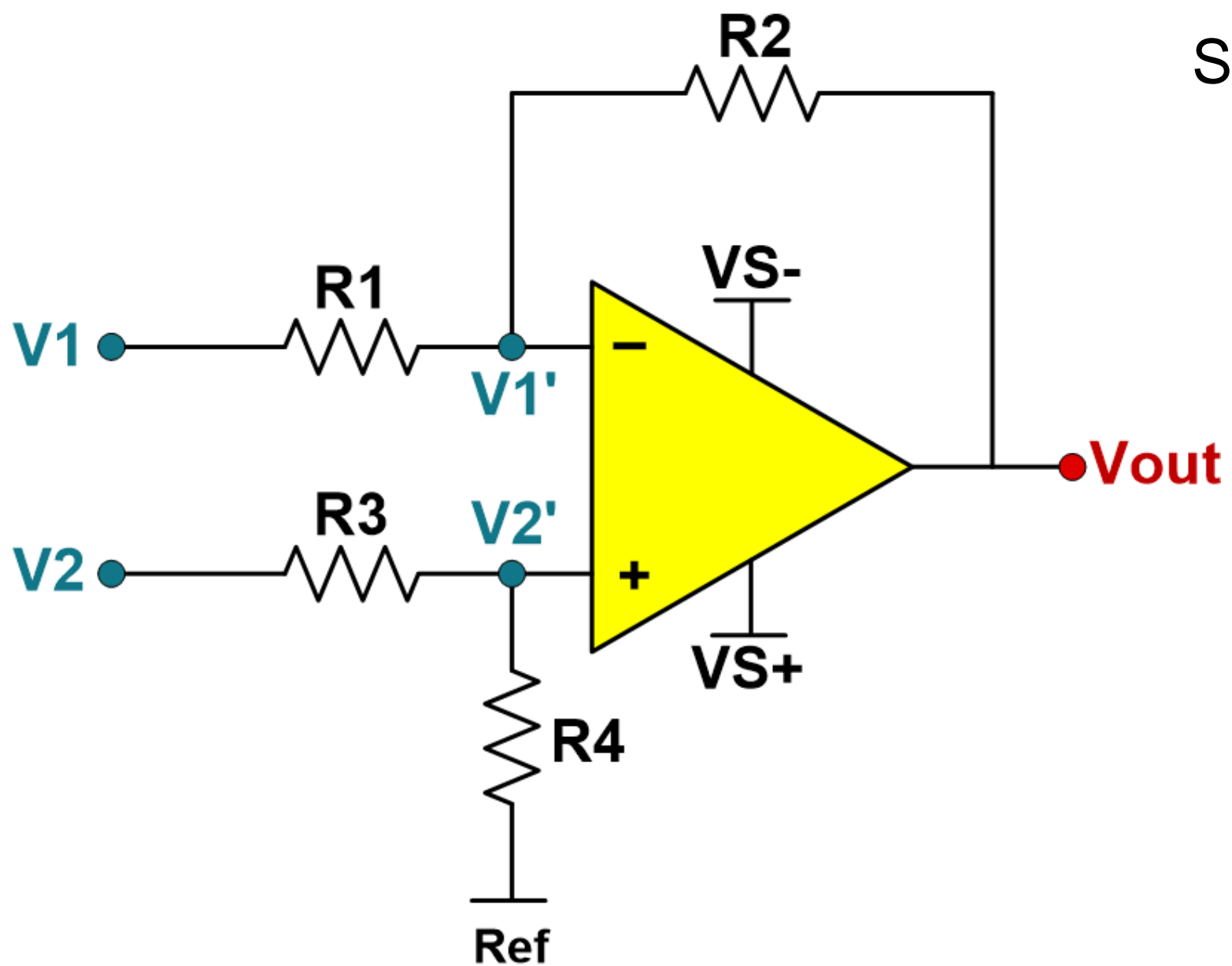
# Instrumentation Amplifier (IA) topologies: one-amp use cases

TI Precision Labs – Instrumentation Amplifiers

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Prepared by Tamara Alani

# One-amp IA topology – Simplified equation



Simplified equation:

$$V_{out} = \left( \frac{R2}{R1} \right) \times (V2 - V1) + Ref$$

*“Differential gain, Ad”*

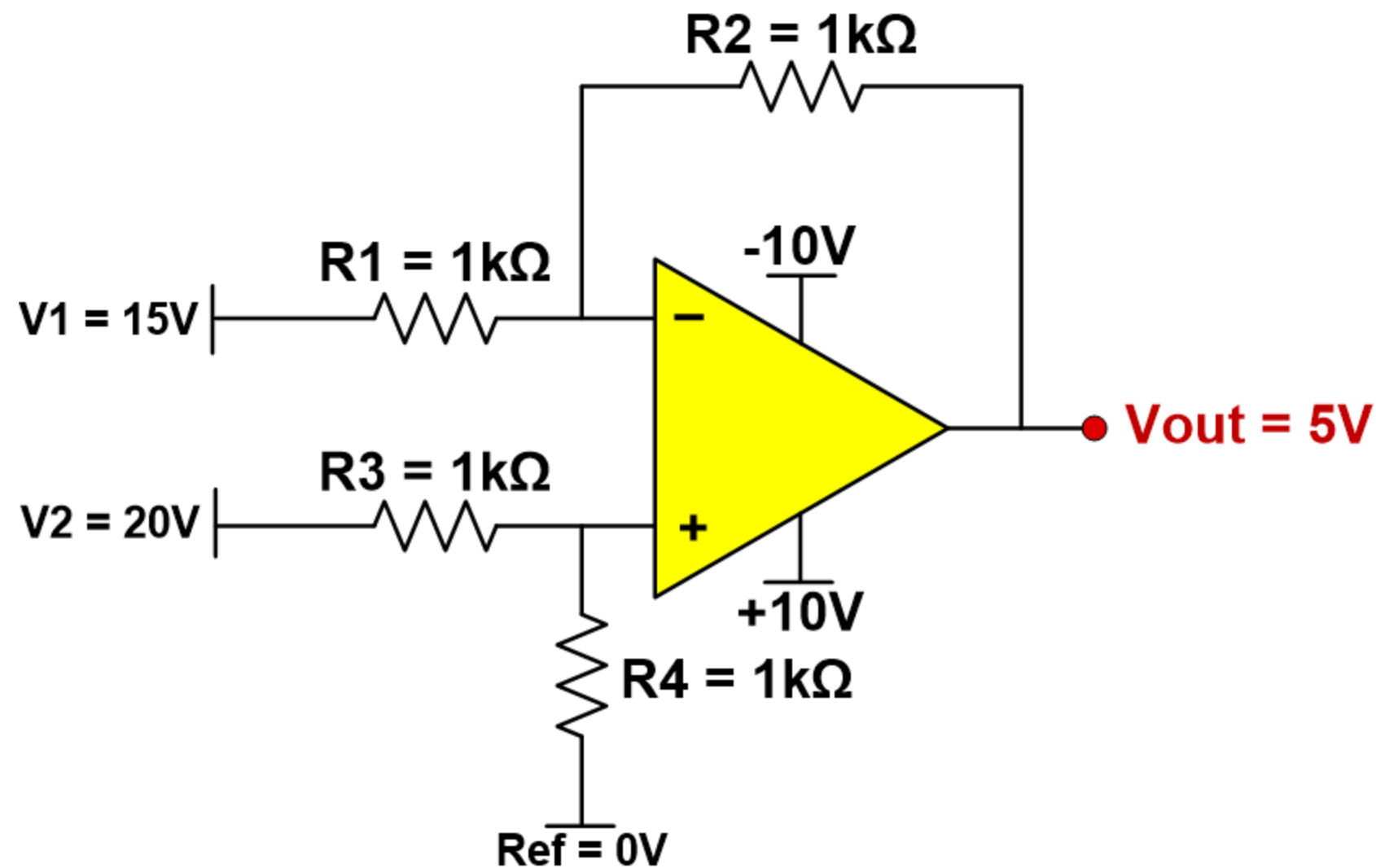
*“Differential input voltage, V<sub>d</sub>”*

$$V_{out} = A_d \times V_d + Ref$$

# Difference amplifier – Use case examples

- **High input voltages in unity gain**
  - Input voltages that exceed the supply rails
- **High gain**
  - Amplifying a very small differential input voltage
- **Attenuation and high input voltages**
  - How to attenuate signals in the presence of large common mode voltages
- **Level-shifting**
  - Adding a reference voltage to level-shift the output

# Difference amplifier – High differential voltages



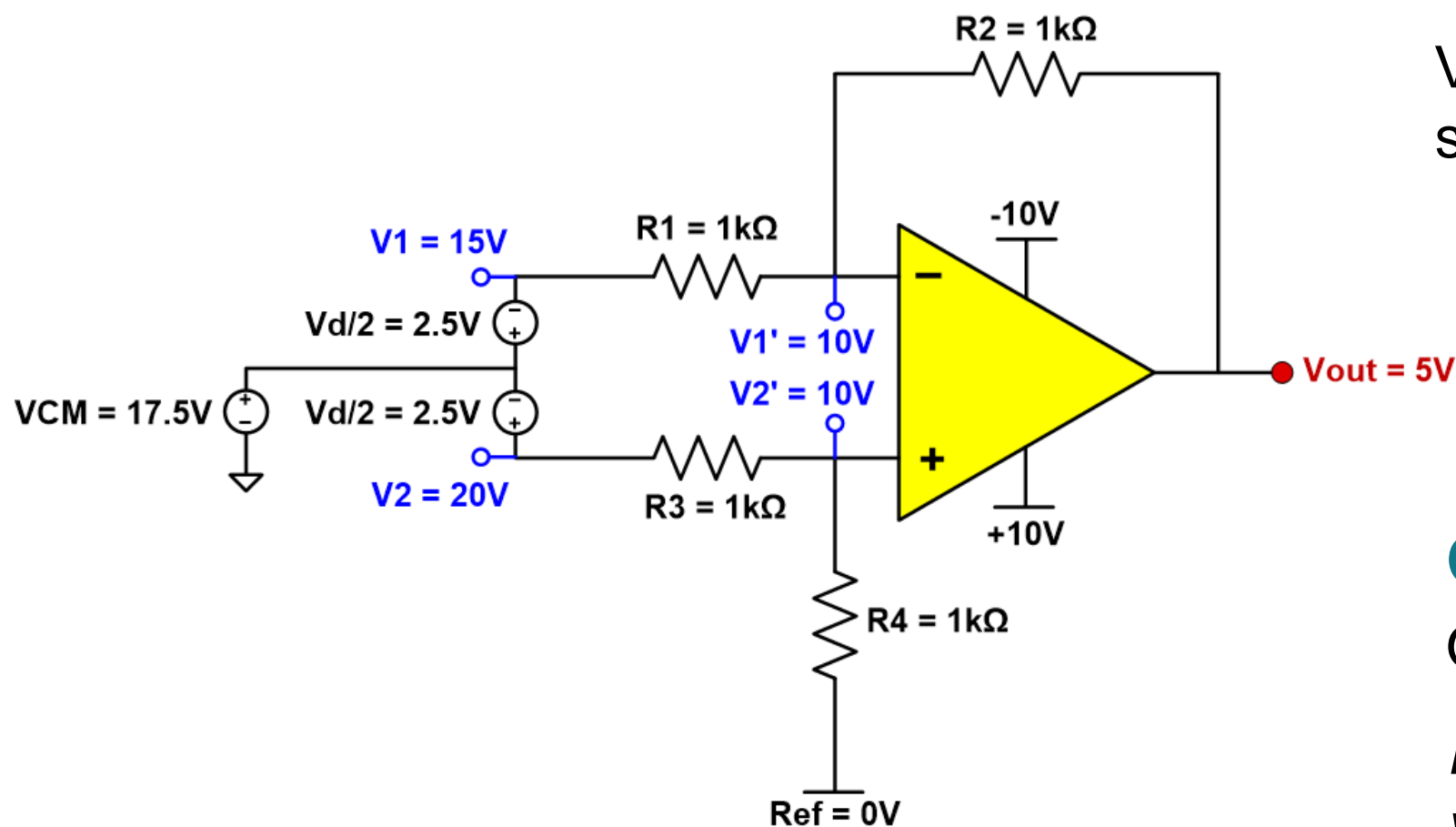
# Input and output – Voltage swing limitations

- Common mode input voltage range = **input voltage range**
  - Relative to the positive and negative power supplies
  - Exceeding the input range leads to non-linear behavior
- **Output voltage swing**
  - Relative to the positive and negative power supplies
  - Exceeding the linear output range of the amplifier leads to non-linear behavior
- **Rail-to-Rail input and output amplifiers**
  - Rail-to-Rail input (RRI) amplifiers have input ranges which extend *to* and sometimes *beyond* the rail
  - Rail-to-Rail output (RRO) amplifiers have output swings *near* the rails

For more information on op amp input / output limitations: watch this TIPL video:  
[3.1 TI Precision Labs - Op Amps: Input and Output Limitations](#)



# Difference amplifier – High differential voltages



## Input analysis:

Input voltage range:  $-10V \leq V_{in} \leq +10V$

$V1'$  and  $V2'$  must be between the input voltage swing (as high as 10V, and as low as -10V)

$$V2' = \frac{R4}{R3 + R4} \times V2 = \frac{V2}{2}$$

$$V1' \text{ tracks } V2', \text{ so } V1' = \frac{V2}{2}$$

## Output analysis:

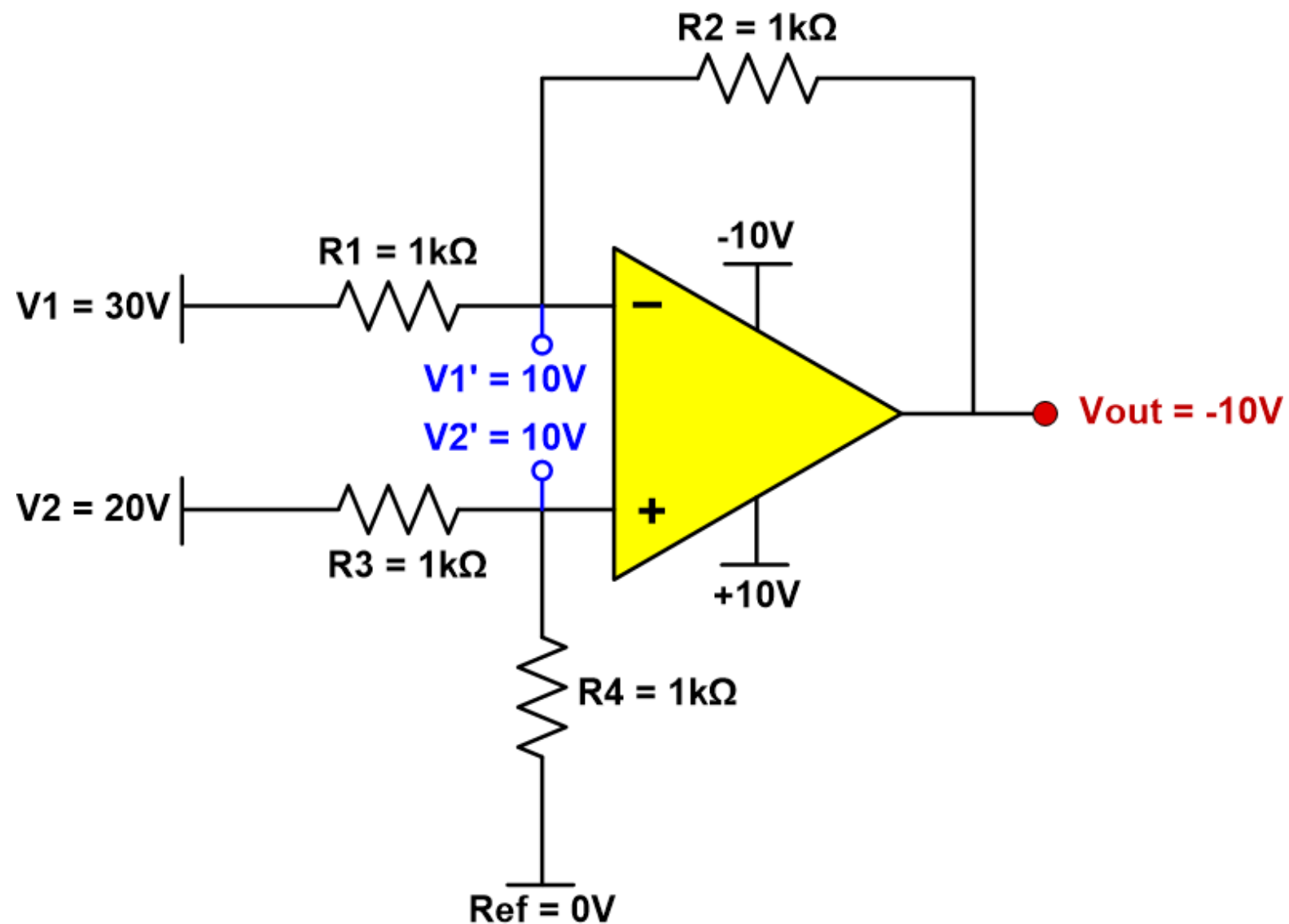
Output voltage swing:  $-9.9V \leq V_{out} \leq +9.9V$

*For simplicity, let us assume the output voltage swing is:  $-10V \leq V_{out} \leq +10V$*

Output equation:  $V_{out} = A_d \times V_d + Ref$

$$V_{out} = V_d$$

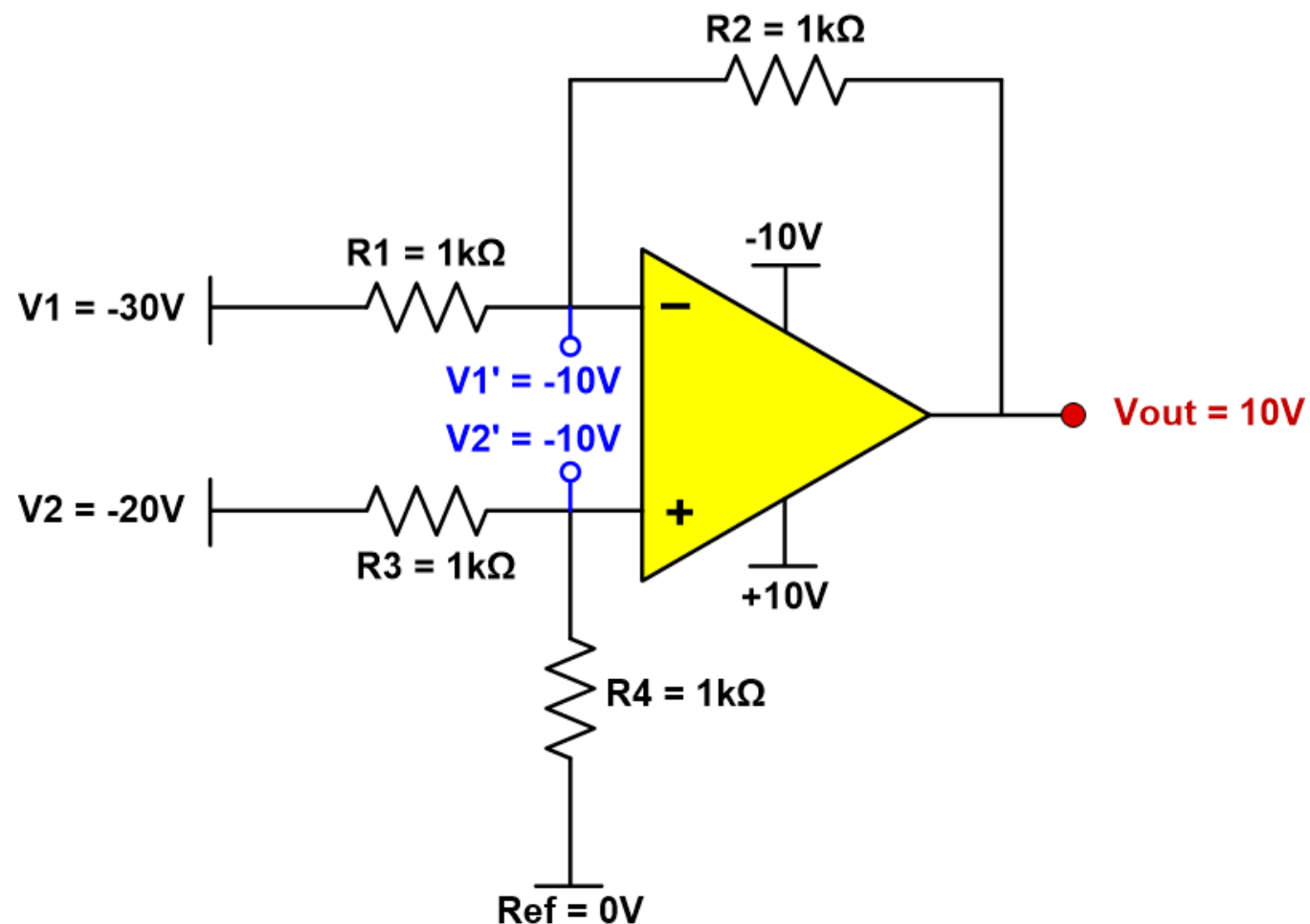
# Difference amplifier – Maximum voltage ranges



## Question:

If  $V_2$  can be as high as  $20\text{V}$  and as low as  $-20\text{V}$  and  $V_{out}$  can be as high as  $10\text{V}$  and as low as  $-10\text{V}$ , what range can  $V_1$  be?

# Difference amplifier – Defining VCM



## Answer:

- When  $V2 = -20V$ ,  $V1$  can be  $-30V$  to yield a linear output of  $10V$
- When  $V2 = +20V$ ,  $V1$  can be  $+30V$  to yield a linear output of  $-10V$

VCM is defined as:

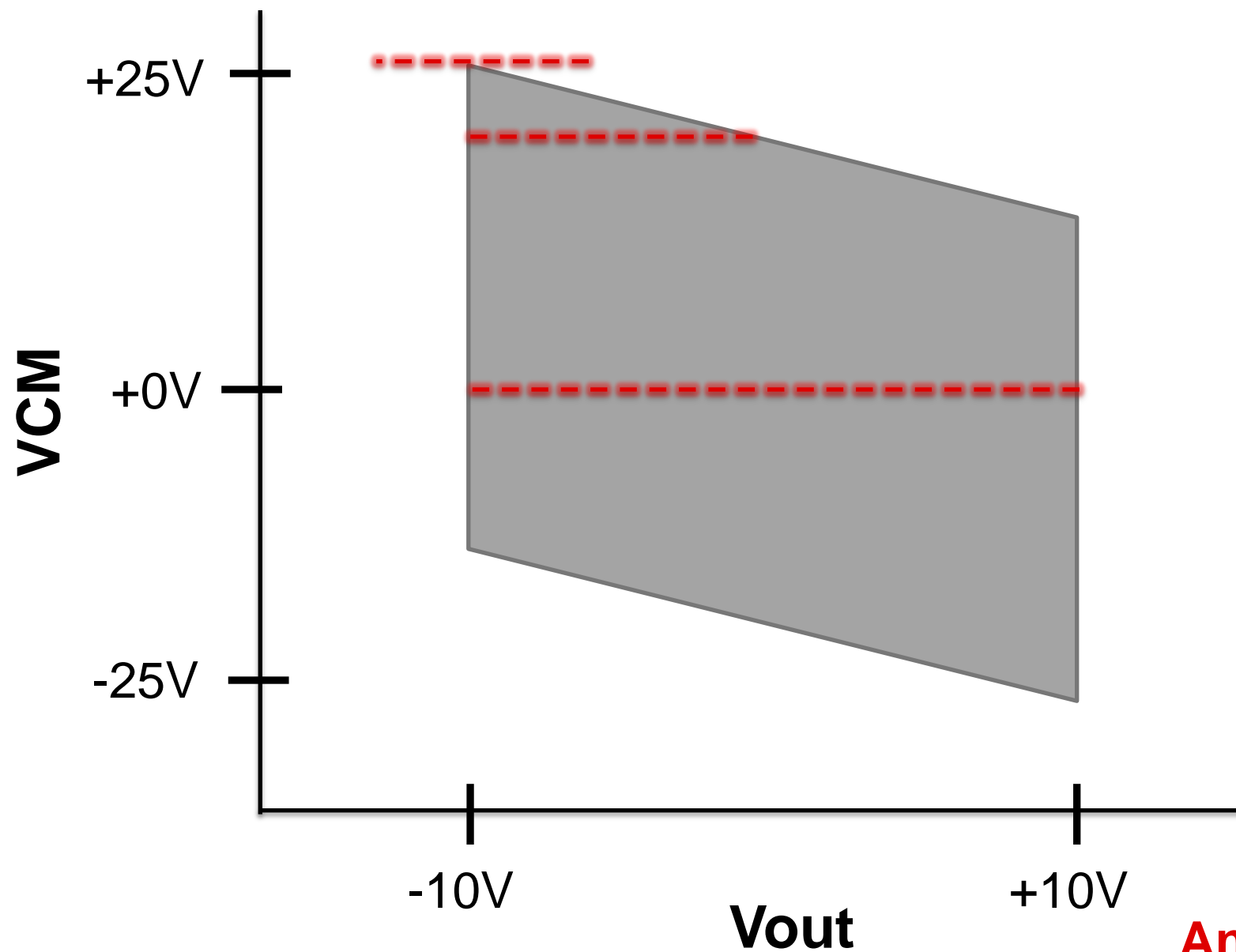
$$VCM = \frac{V1 + V2}{2}$$

When  $V1 = 30V$  and  $V2 = 20V$ , **VCM = 25V**

When  $V1 = -30V$  and  $V2 = -20V$ , **VCM = -25V**



# Linear behavior – VCM vs Vout Boundary plot



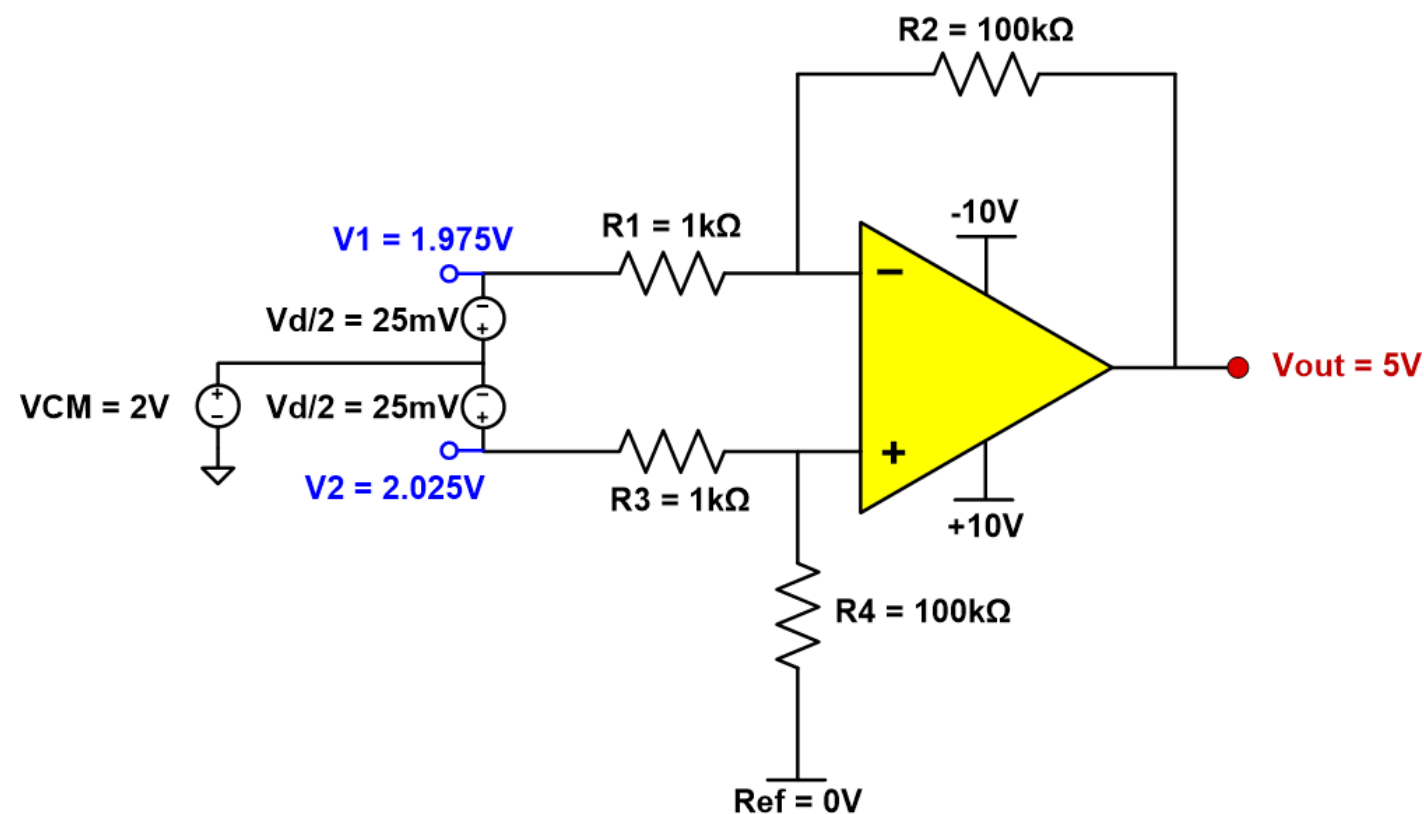
**Boundary plots** graphically show a designer the usable range of an IA by analyzing the internal nodes.

- Y-axis: **VCM** = -25V to +25V
- X-axis: **Vout** = Vd = -10V to +10V

VCM	Vout = Vd
25V	-10V
20V	-10V to 0V
0V	-10V to +10V

[Analog engineer's calculator → INA Vout vs VCM](#)

# Difference amplifier – High gain



Remember, we can match R1 to R3 and R2 to R4 to arrive at the general equation:

$$V_{out} = V_d \times A_d + Ref$$

$$A_d = \text{signal gain} = R_2/R_1$$

Assume we have an input differential voltage ( $V_d$ ) of 50mV and want to gain this up to 5V at the output.

$$\text{Calculate gain: } A_d = \frac{V_{out}}{V_{in}} = \frac{5V}{50mV} = 100V/V$$

$$\text{Choose } R_2 \text{ and } R_1 \text{ such that } \frac{R_2}{R_1} = 100$$

We can choose:

$$R_2 = R_4 = 100k\Omega \text{ and } R_1 = R_3 = 1k\Omega$$

# Difference amplifier – Attenuation

$$V_{out} = A_d \times V_d + Ref$$

$$A_d = \text{signal gain} = \frac{R_2}{R_1}$$

Need:  $R_2 = R_4$  and  $R_1 = R_3$

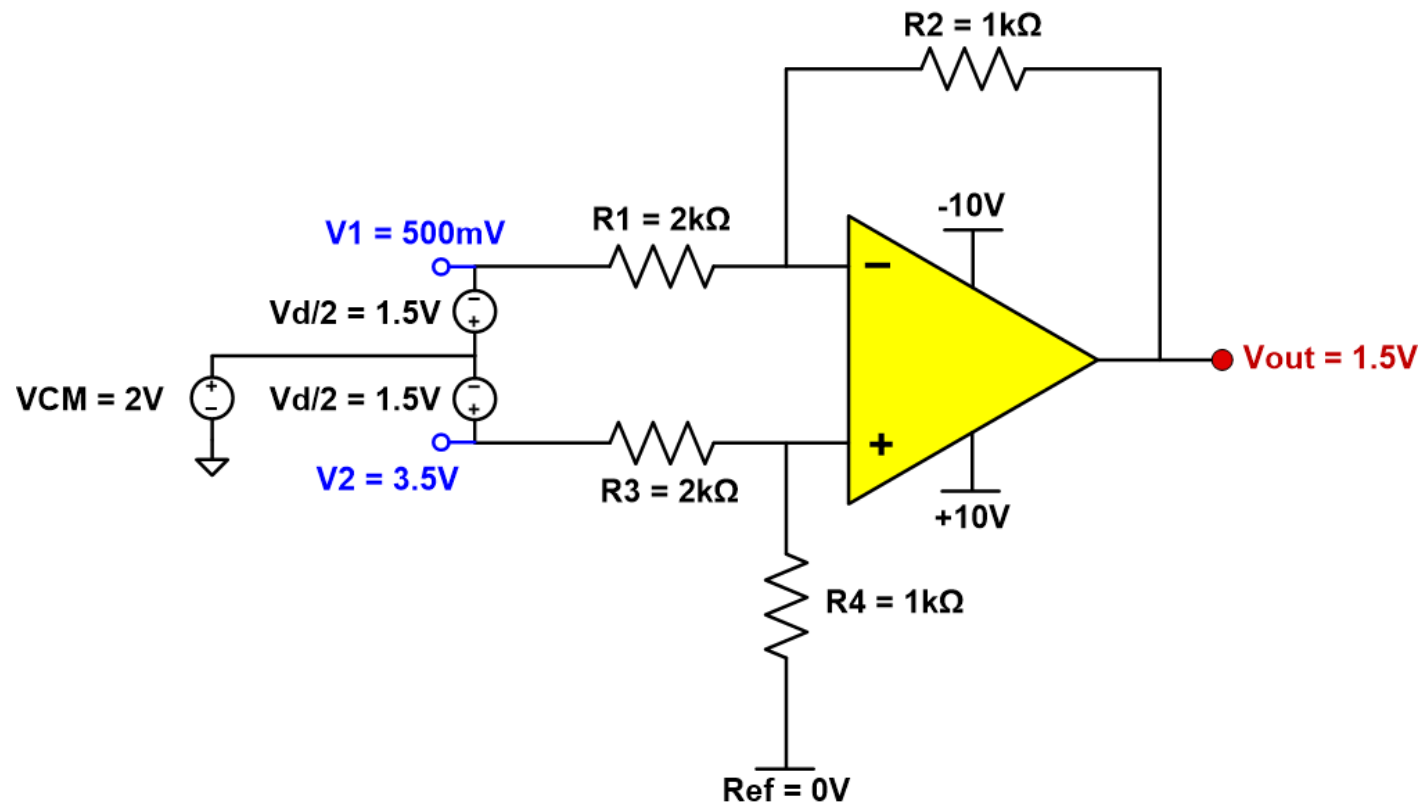
Assume we have  $V_d = 3V$  and we want to attenuate this to 1.5V at the output.

$$\text{Calculate gain: } A_d = \frac{V_{out}}{V_{in}} = \frac{1.5V}{3V} = 0.5V/V$$

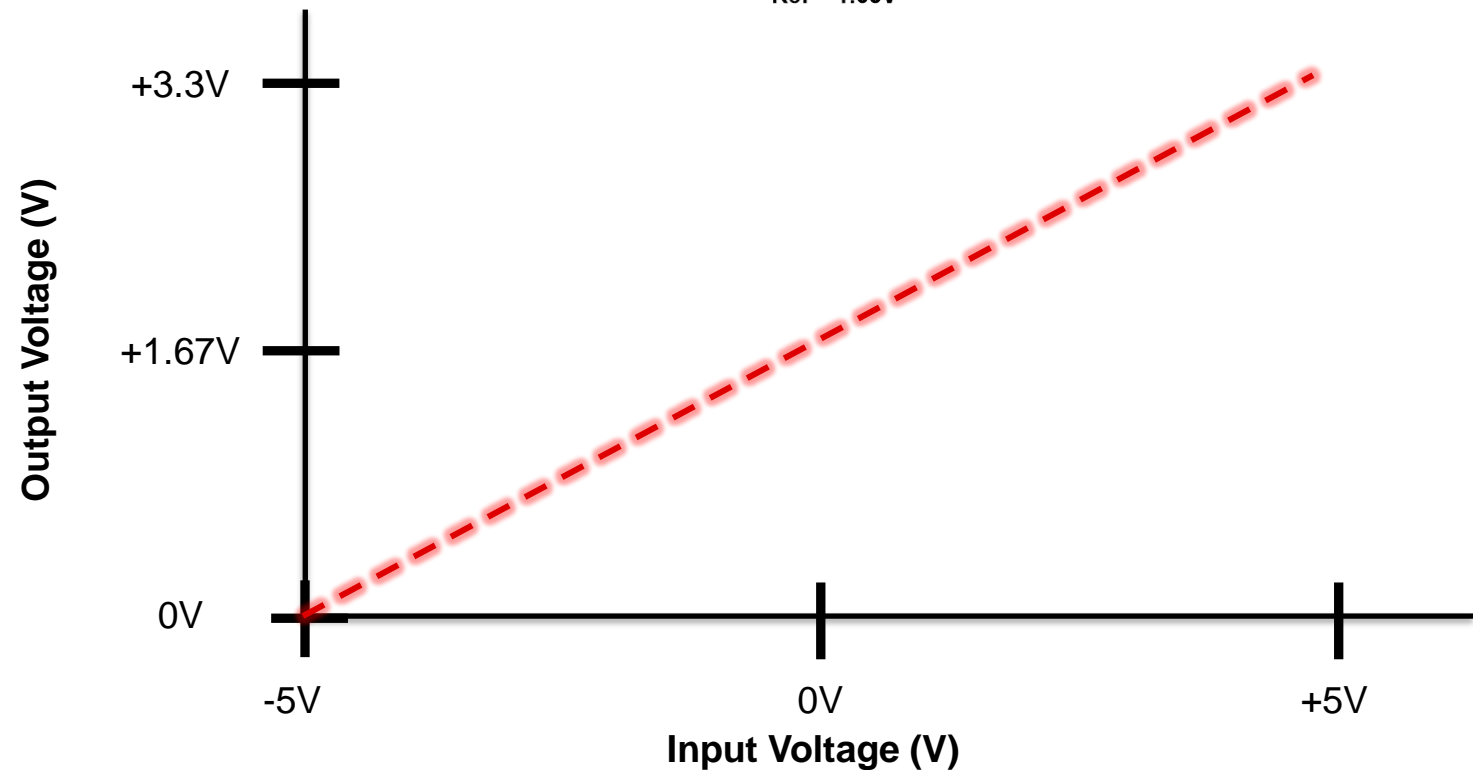
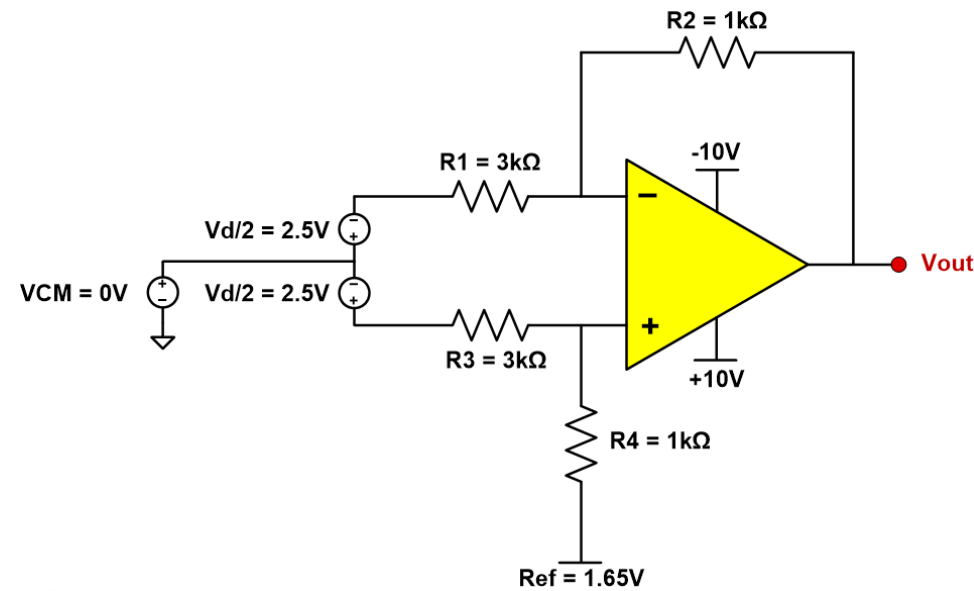
Choose  $R_2$  and  $R_1$  such that  $\frac{R_2}{R_1} = 0.5$

We can choose:

$$R_2 = R_4 = 1k\Omega \text{ and } R_1 = R_3 = 2k\Omega$$



# Difference amplifier – Level shifting



Example:

$V_d$ : -5V to 5V

$V_{out}$ : 0V to 3.3V

- Solve for Gain:

$$\text{Gain} = \frac{\Delta V_{out}}{\Delta V_d} = \frac{3.3}{10} = \frac{1}{3} = \frac{V_{outmax} - V_{outmin}}{V_{inmax} - V_{inmin}}$$

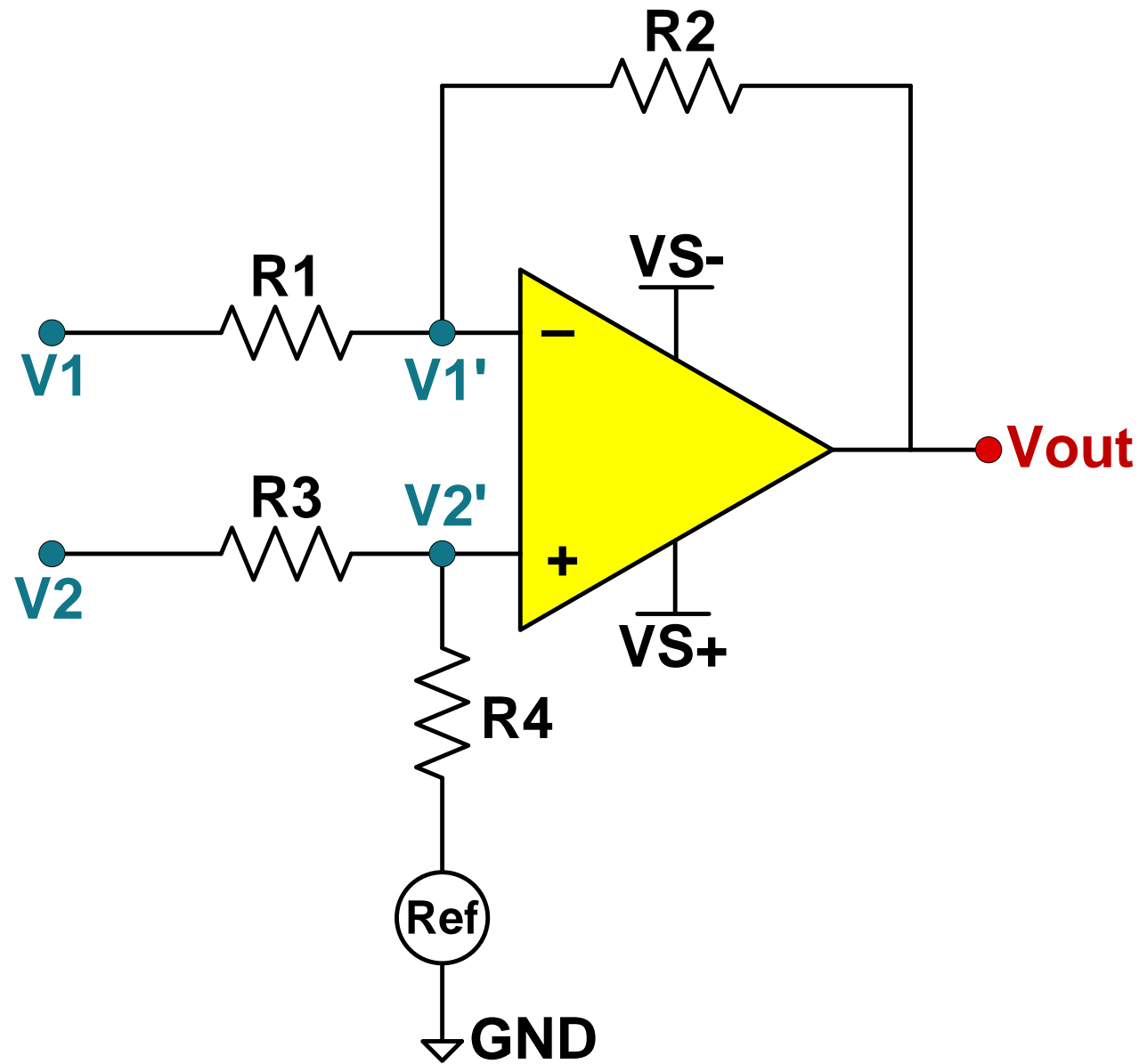
Choose  $R_2 = R_4 = 1\text{k}\Omega$ ,  $R_1 = R_3 = 3\text{k}\Omega$

- Solve for Ref:

$$V_{out} = A_d \times V_d + \text{Ref}$$

$$0 = \frac{1}{3} \times (-5) + \text{Ref} \rightarrow \text{Ref} = 1.67\text{V}$$

# One amp IA topology – Design summary



## Design challenges:

- ▶ Low input impedance
- ▶ Challenge to match resistances

## Summary:

If you have an application where you have low impedance sources and high common mode voltages, consider a one-amp IA (difference amplifier) topology

**Thanks for your time!**  
**Please try the quiz.**



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