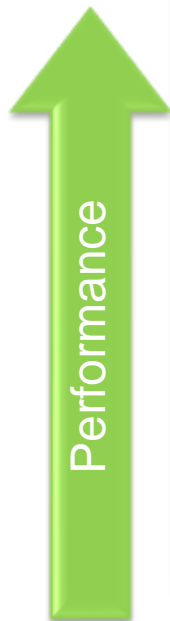


# TI OpenVX™ (TIOVX): Implementation details

# TIOVX - OpenVX implementation on TI SoC

**GOAL:** Help users easily maximize performance on TI platforms while minimizing development cost.



## Graph-based model

Defined and analyzed at initialization time for optimal run time and latency

## True heterogeneous compute

Abstracted access to heterogeneous cores, such as C7x/MMA, VPAC and DMPAC

## Optimized libraries

Fully optimized OpenVX 1.1 kernels

## DMA integration

Sample DMA interface provided for tiled access



## Open standard

Conformant to **OpenVX v1.1**

## Software abstraction

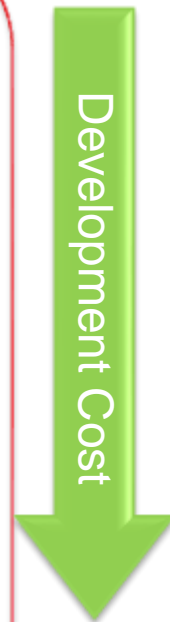
Application works across SW platforms from Linux, QNX and TI-RTOS

## Hardware abstraction

Application works across Jacinto family of SoCs

## Ease of use

PC-based development environment  
PyTIOVX tool for easy kernel integration



**Result:** Full entitlement on TI SoCs through performance portable OpenVX interface

# For more information

- Jacinto 7 Processor SDK Automotive download:  
<http://www.ti.com/tool/PROCESSOR-SDK-JACINTO-DRA8X-TDA4X>
- Processor SDK Linux Automotive (PSDKLA) user guide:  
[\\${PSDKLA\\_INSTALL\\_PATH}/docs/linux/index.html](${PSDKLA_INSTALL_PATH}/docs/linux/index.html)
- Processor SDK RTOS Automotive (PSDKRA) user guide:  
[\\${PSDKRA\\_INSTALL\\_PATH}/index.html](${PSDKRA_INSTALL_PATH}/index.html)
- For additional questions, refer to the E2E community forums:  
<https://e2e.ti.com/support/processors/f/791>



**©2020 Texas Instruments Incorporated. All rights reserved.**

The material is provided strictly "as-is" for informational purposes only and without any warranty.  
Use of this material is subject to TI's **Terms of Use**, viewable at [TI.com](https://www.ti.com)